

FIG. 2

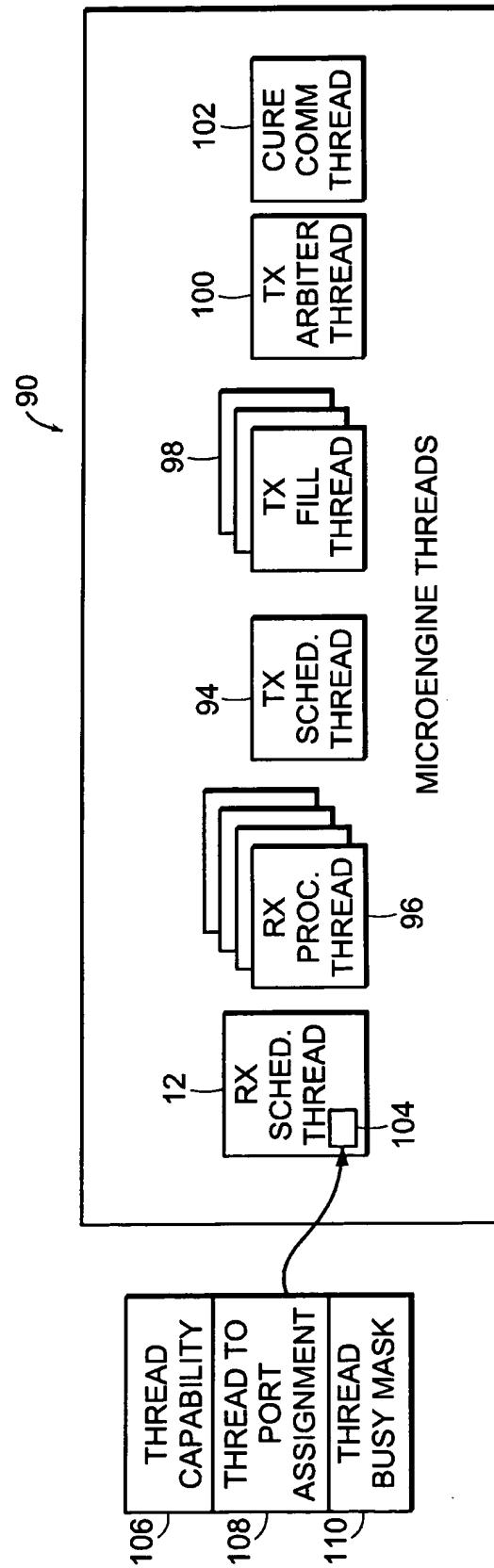


FIG. 3

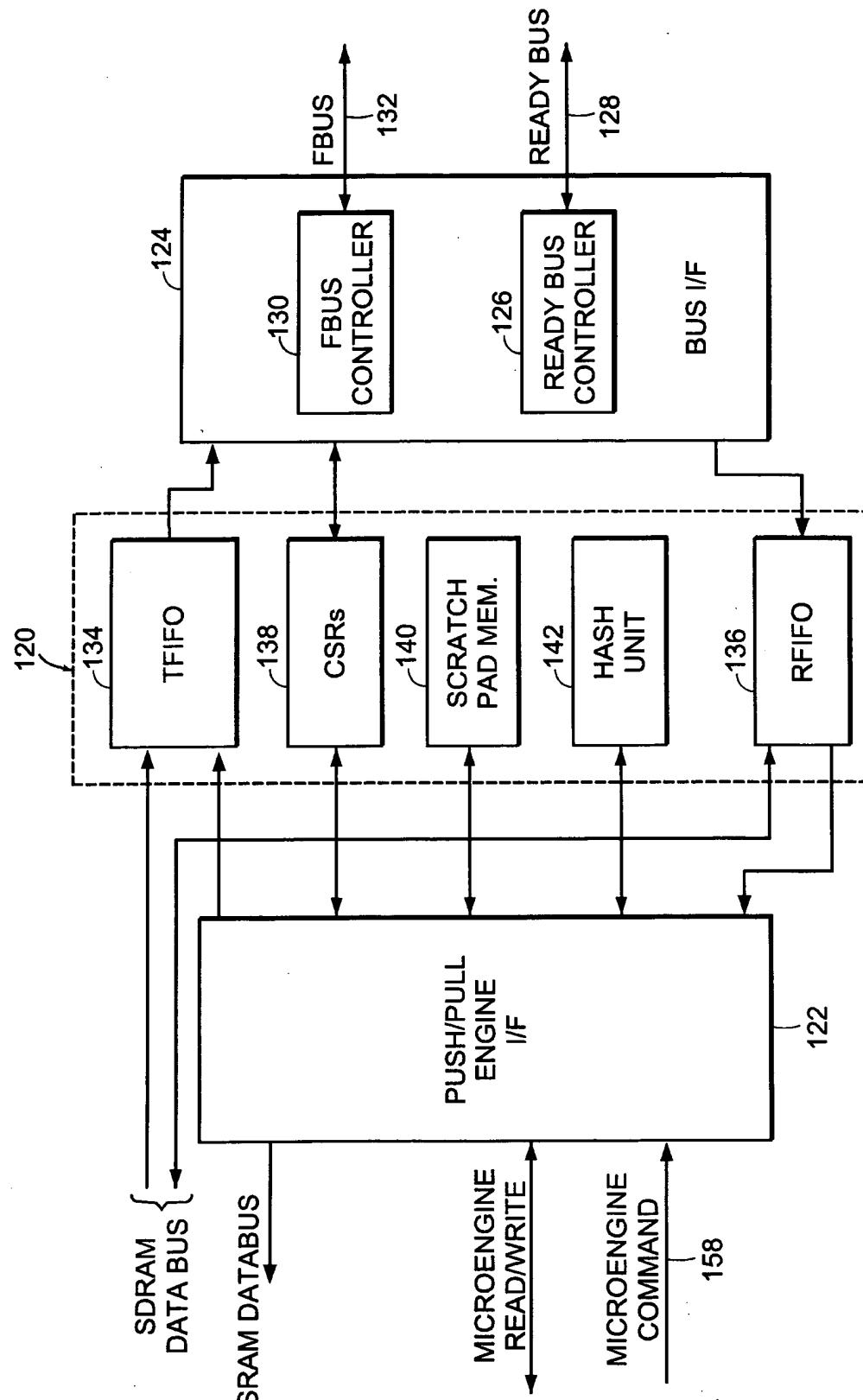


FIG. 4

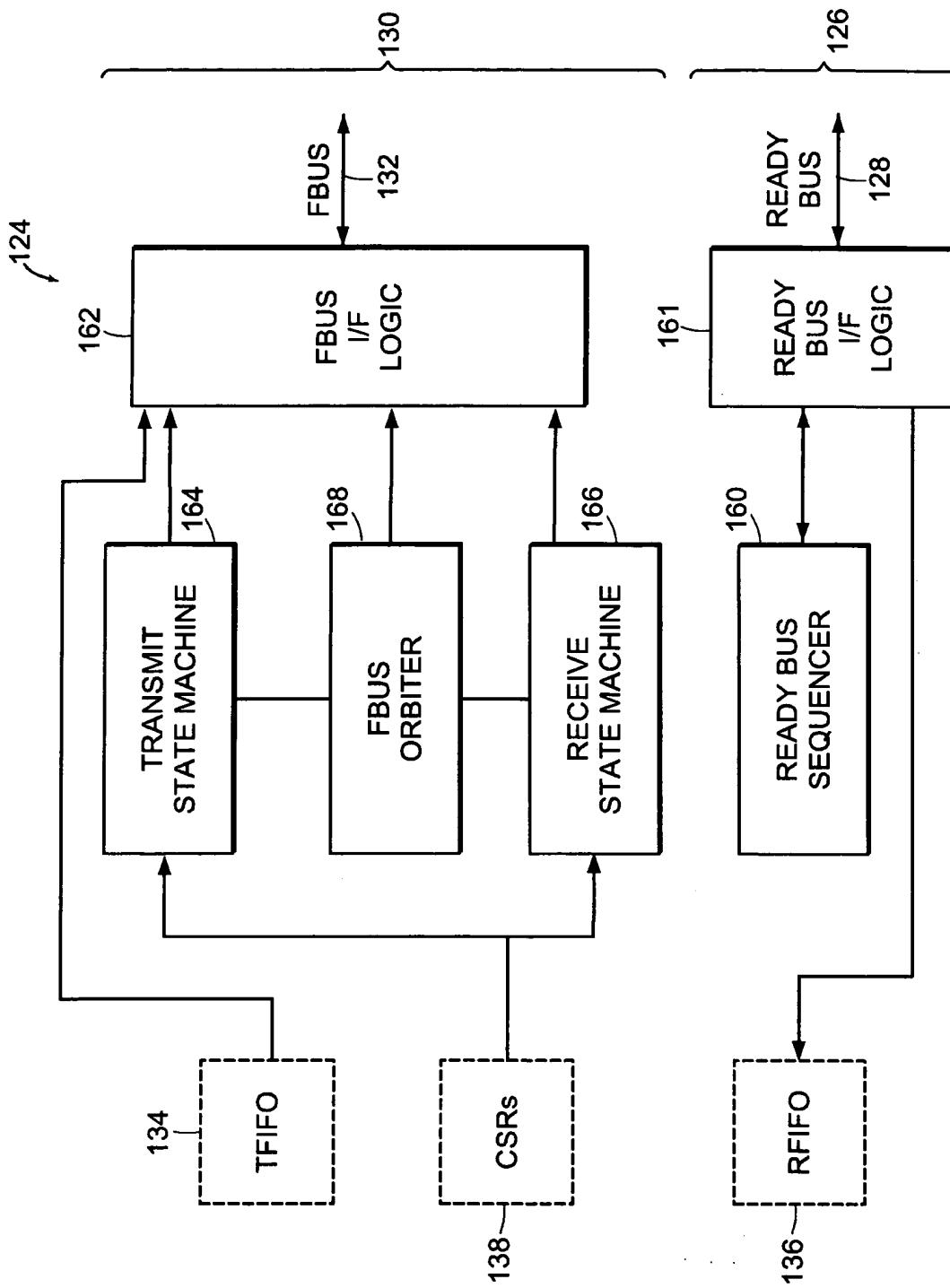


FIG. 5



Appln No.: 09/176,303
Applicant(s): Robert Wolrich et al.

Page 6 of 12

PORT BLOCKING TECHNIQUE FOR MAINTAINING RECEIVE
PAKET ORDERING FOR A MULTIPLE ETHERNET PORT
SWITCH

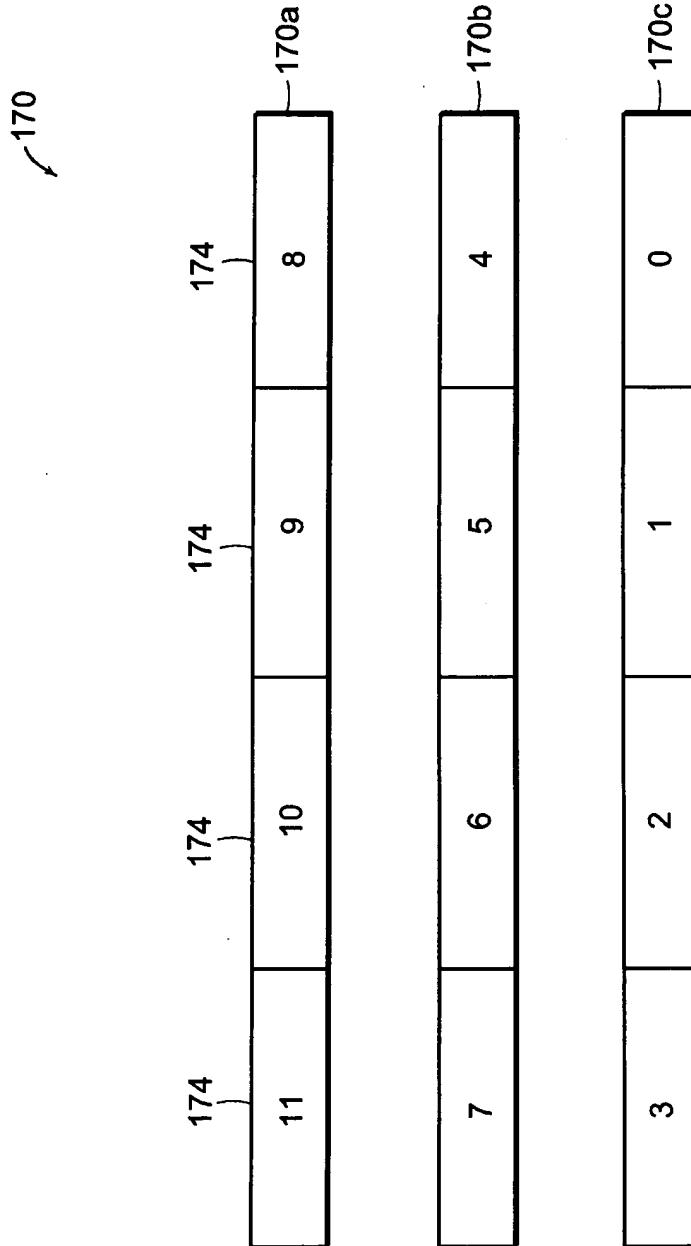


FIG. 6A



Appln No. 09/476,303

Page 7 of 22

Applicant: Gilbert Wolrich et al.

PORT BLOCKING TECHNIQUE FOR MAINTAINING RECEIVE
PAKET ORDERING FOR A MULTIPLE ETHERNET PORT
SWITCH

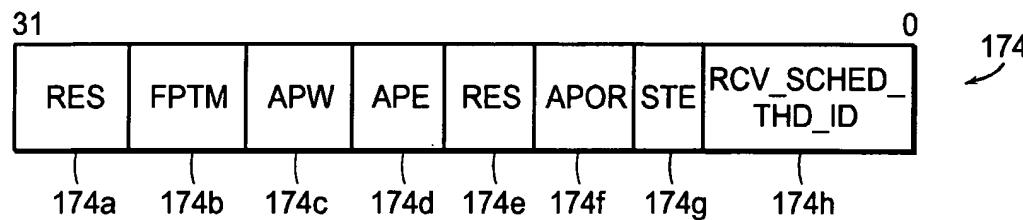


FIG. 6B



FIG. 6C

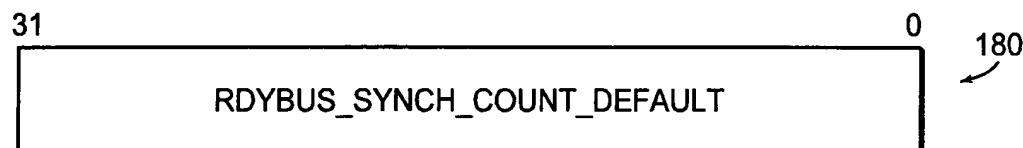


FIG. 6D

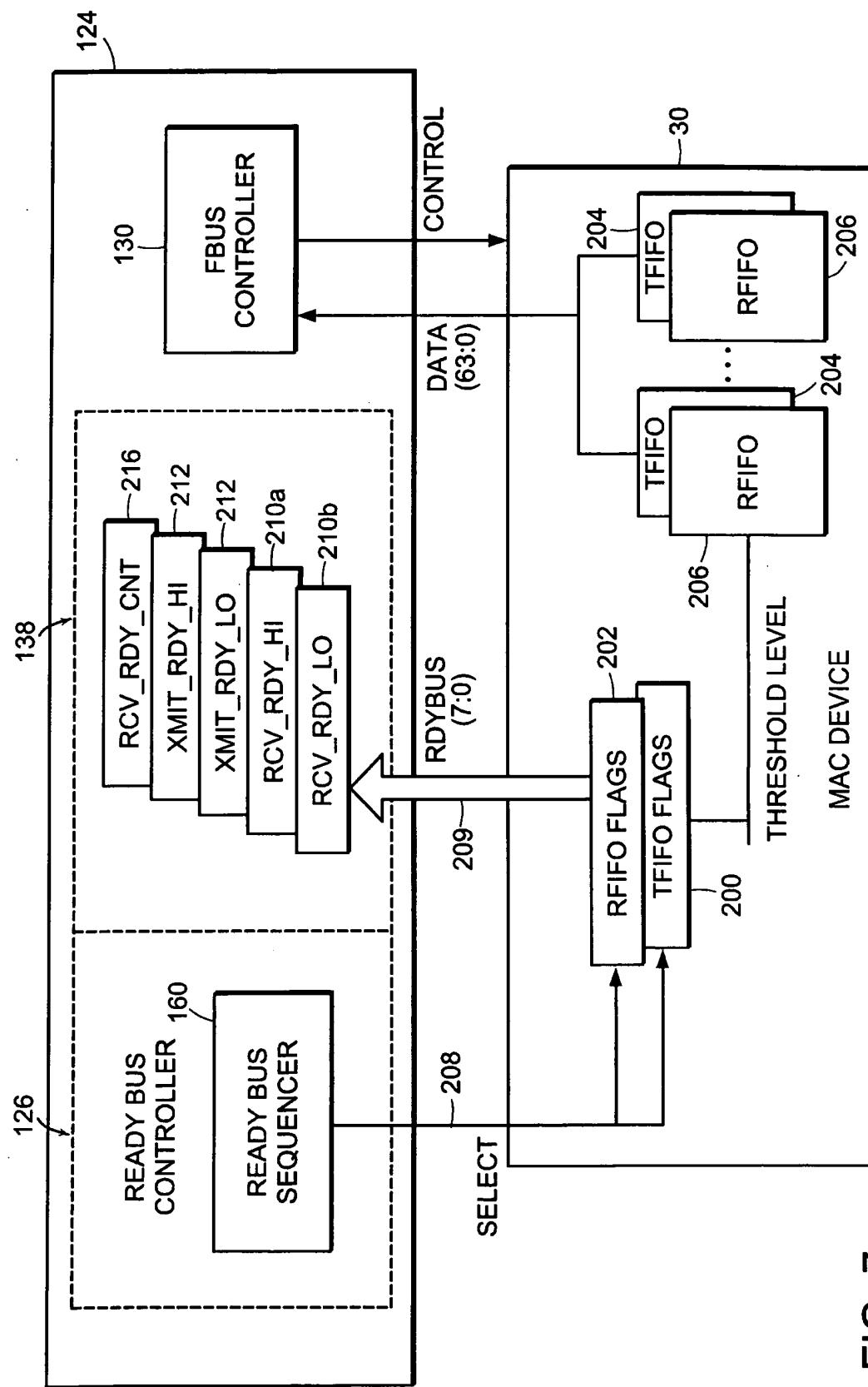


FIG. 7



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PORT BLOCKING TECHNIQUE FOR MAINTAINING RECEIVE PACKET ORDERING FOR A MULTIPLE ETHERNET PORT SWITCH

Page 9

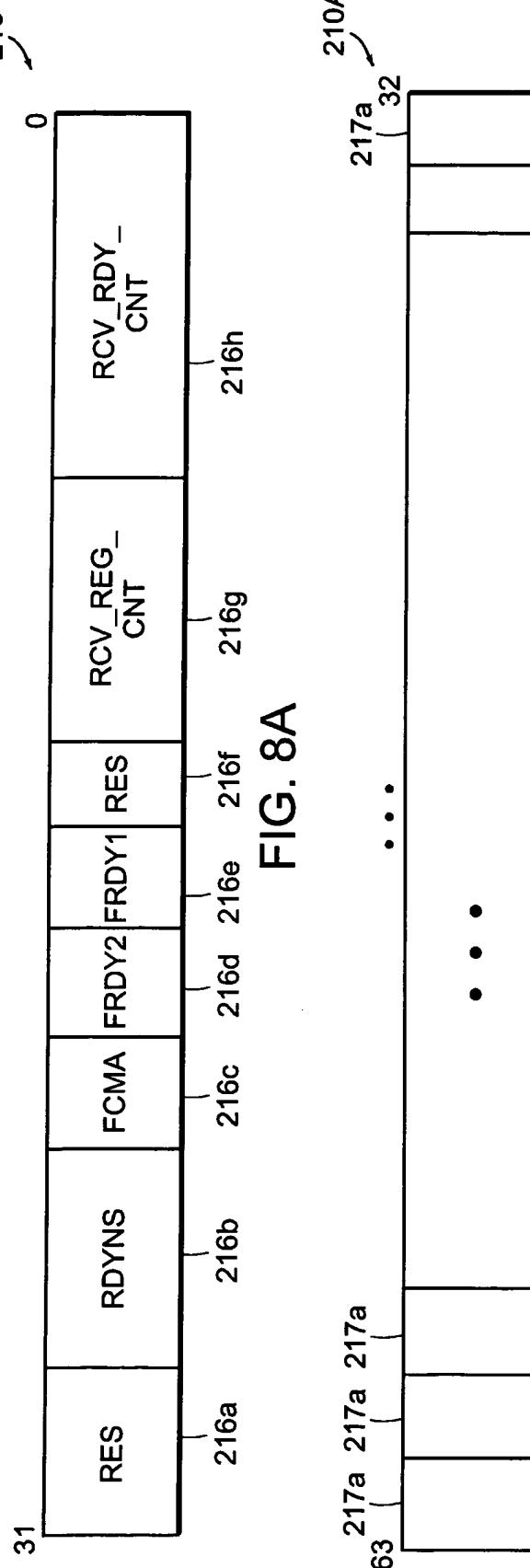
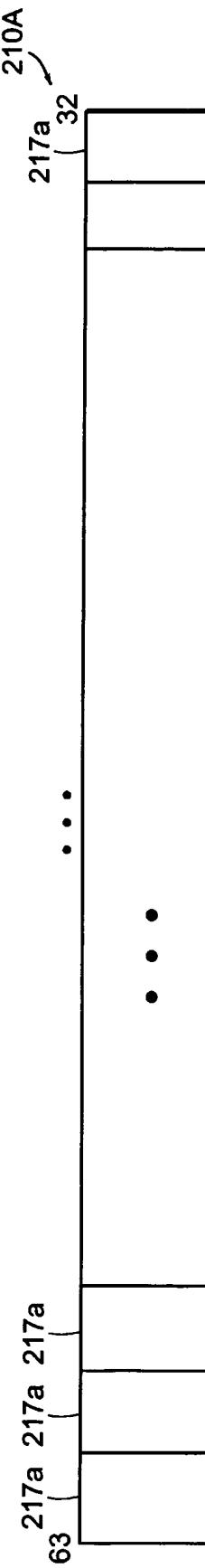
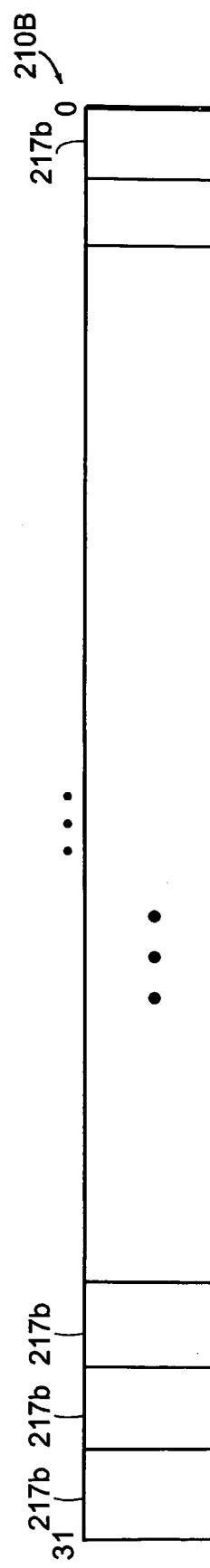


FIG. 8A



88



8C
FIG

JUN 18 2003

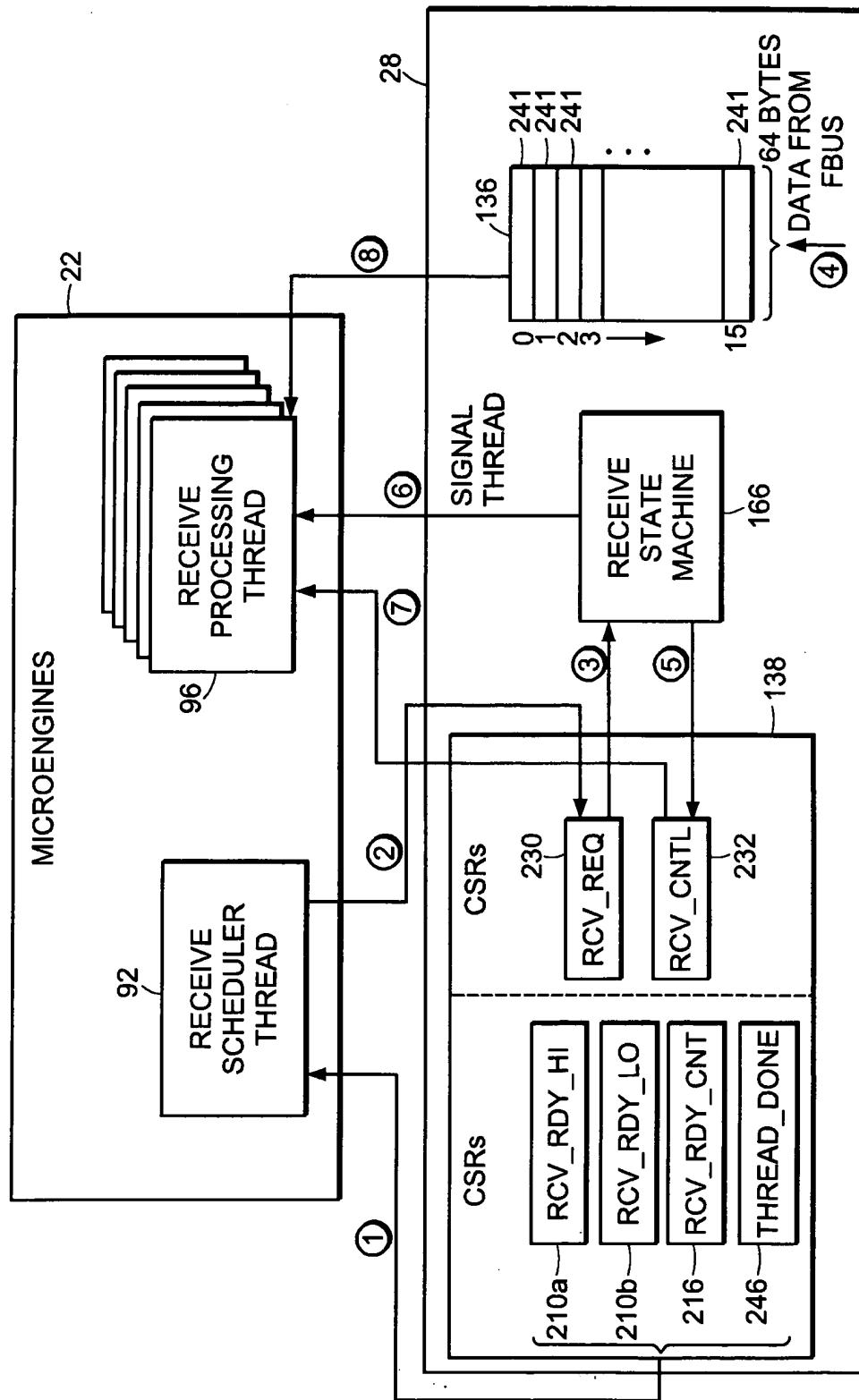


FIG. 9



Appln. No. 09/476,303

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PORT BLOCKING TECHNIQUE FOR MAINTAINING RECEIVE
PAKET ORDERING FOR A MULTIPLE ETHERNET PORT
SWITCH

Page 1 of 12

230

| | | | | | | | | | | | |
|------|------|------|------|------|------|------|------|------|-------|-------|------|
| 230a | 230b | 230c | 230d | 230e | 230f | 230g | 230h | 230i | 230j | 230l | 230m |
| 231 | RES | FA | | JL | E2 | E1 | FS | NFE | IG FR | STGRS | TID |
| 231 | | | | | | | | | | | RP |

FIG. 10A

232

| | | | | | | | | | | | |
|------|-------|-----------------|--------------|------|------|------|------|------|------|----------------|------|
| 232a | 232b | 232c | 232d | 232e | 232f | 232g | 232h | 232i | 232j | 232k | 232l |
| 233 | THRSG | MACPORT/ THD | SOP/ SEQ# | RF | REAR | SE | FE | EFOR | SNOR | VALID BYTES | EOP |
| 233 | | | | | | | | | | | SOP |
| 233 | | | | | | | | | | | 0 |

FIG. 10B

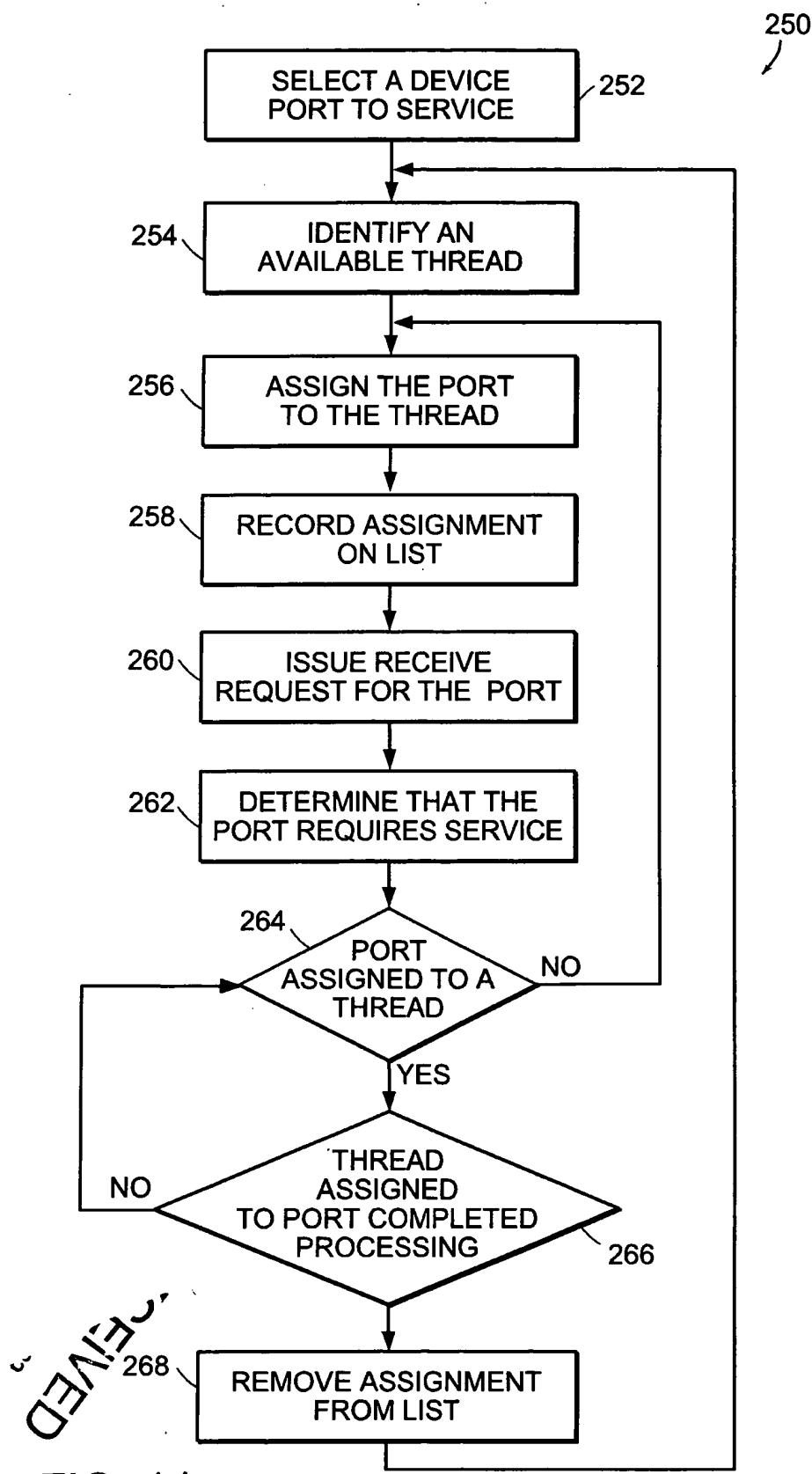


FIG. 11